int z:=0, s;

A state diagram with the following elements:

- **addr_eingabe** (ellipse state, top)
- **idle** s:=0; (circle state, center)
- **read/put** s++; (ellipse state, right)
- **addr_result** (ellipse state, bottom)

Transitions:
- sys_res_n $\cdots\cdots\rightarrow$ idle
- idle $\rightarrow$ addr_eingabe: d_new_eingabe$\uparrow$
- addr_eingabe $\rightarrow$ read/put
- read/put self-loop: $s \le 71$
- read/put $\rightarrow$ idle: $s > 71$
- idle $\rightarrow$ addr_result: d_new_result$\uparrow$
- addr_result $\rightarrow$ read/put